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Fig. 1

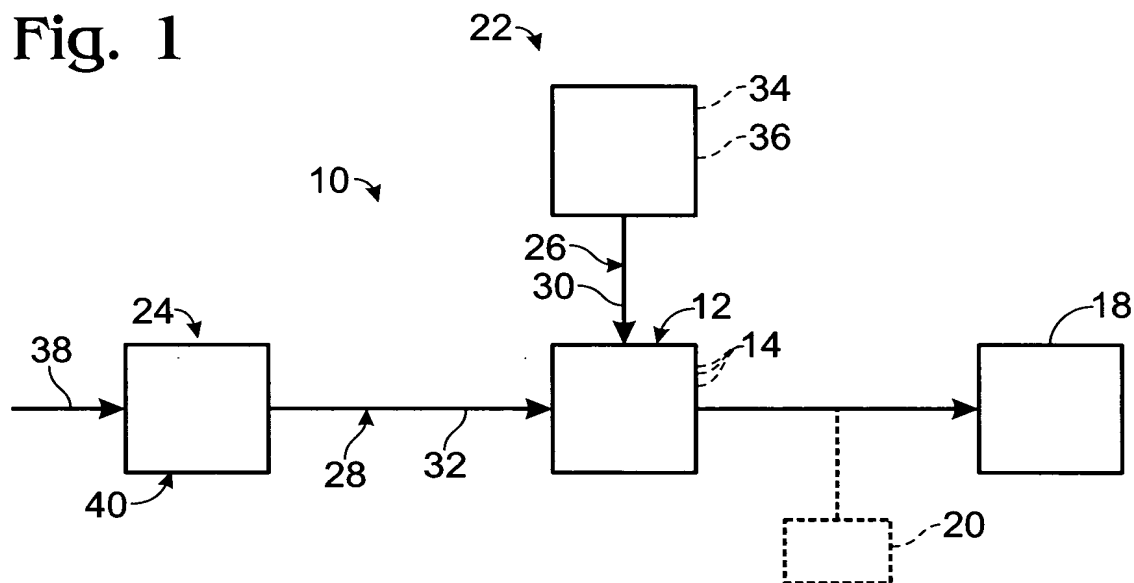
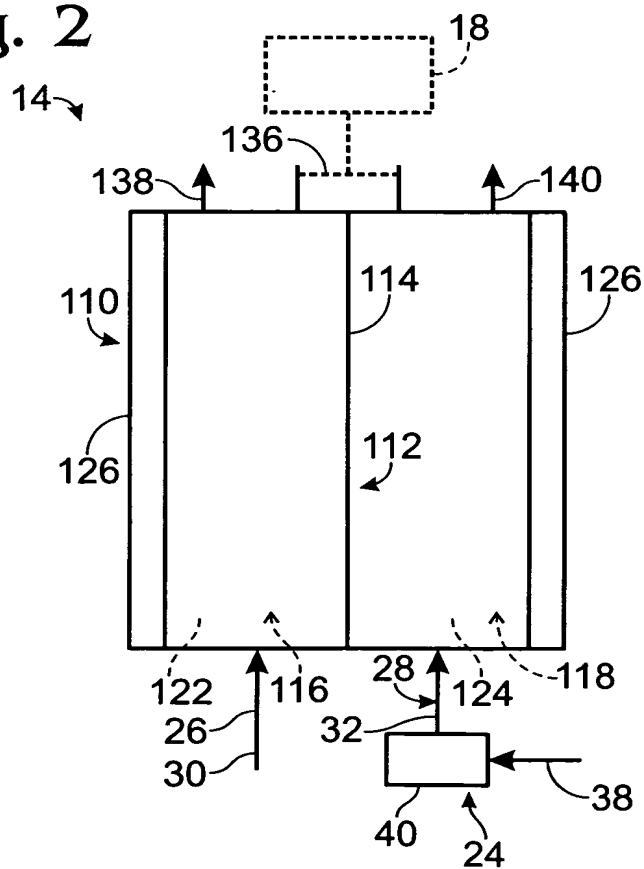


Fig. 2



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Fig. 3

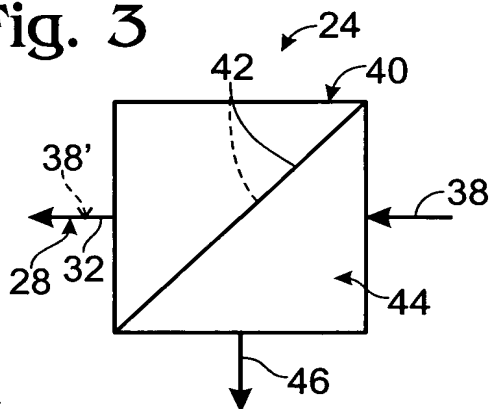


Fig. 4

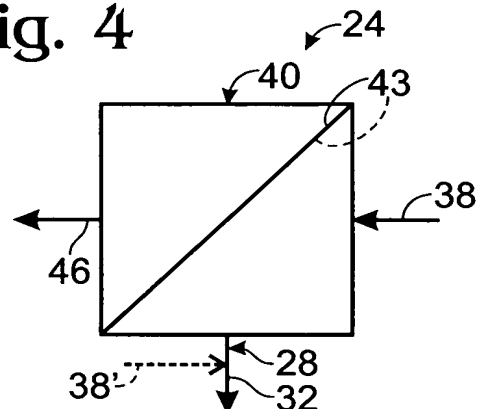


Fig. 5

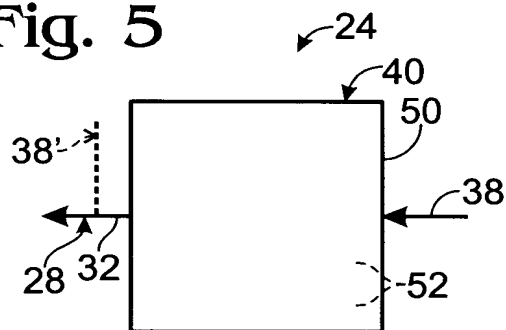


Fig. 6

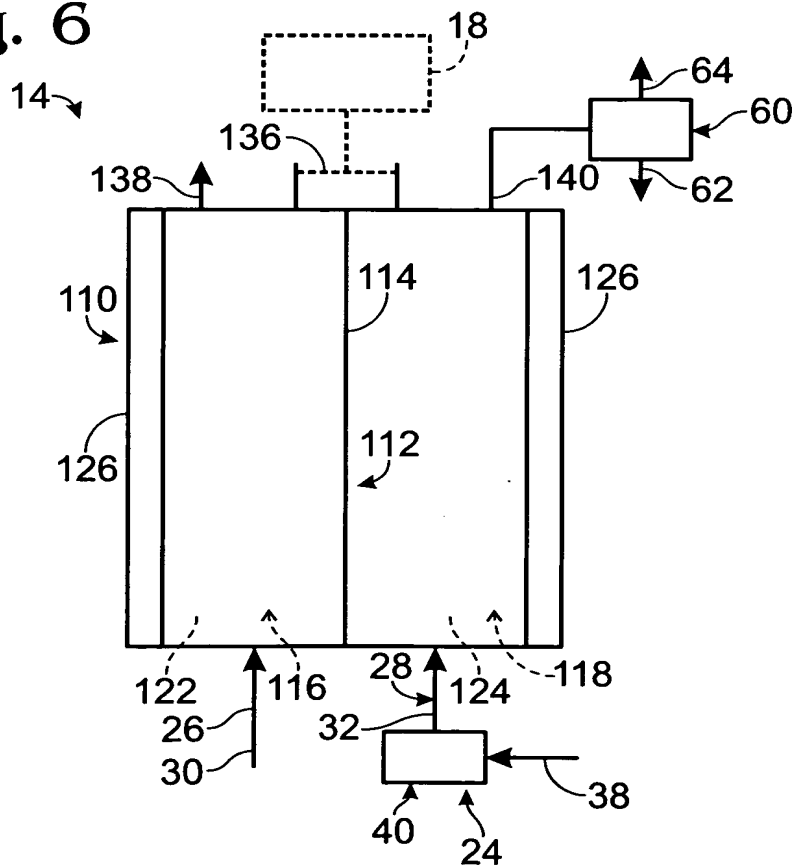
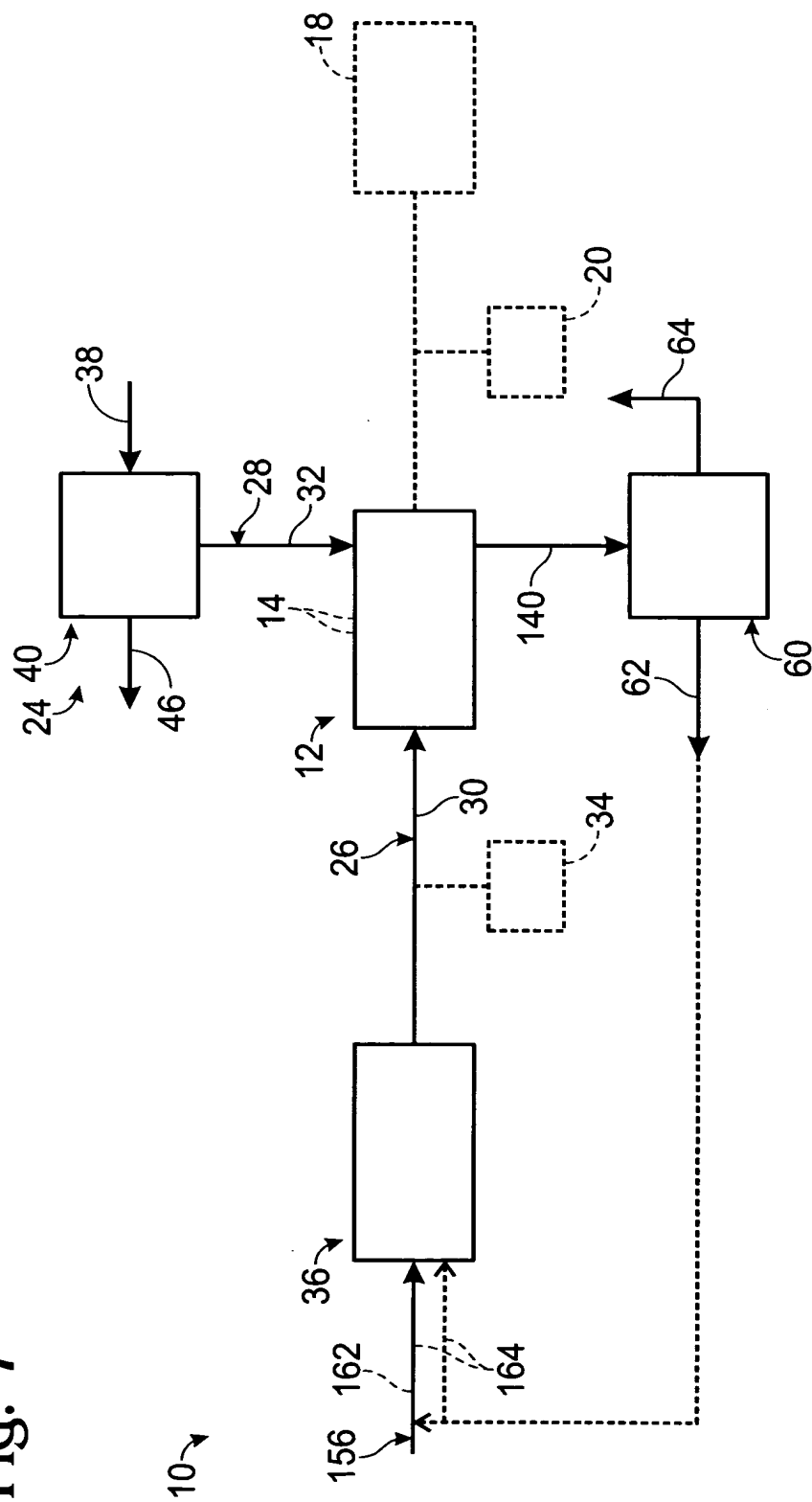


Fig. 7



[illegible]

Diagram illustrating a system architecture. A first block (36) receives inputs 10, 166, 162, 156, and 164. It outputs 152 to a second block (12). The second block (12) receives input 26 and outputs 14. A dashed line 30 connects the two blocks.

A block diagram of a system 36. An input 156 enters from the left and splits into two parallel paths. The first path contains a block 232, followed by a dashed line 234, and then a block 238. The second path contains a block 242, followed by a dashed line 244, and then a block 246. Both paths converge at a block 240 at the bottom. An output 26 exits to the right. A dashed line 238' connects the output of block 238 to block 246. A dashed line 247 connects the output of block 246 to block 240. A dashed line 30 exits from the right side of the system 36.

The diagram shows a system architecture within a rectangular frame. An input signal 10 enters from the left, passing through a block 152. A dashed line 233 is positioned before the first block. The output of the first block is 232, which passes through a block 234 to become 236. A feedback loop 235 branches off from the output 236 and returns to the input 10. The signal 236 then enters a second block 238, which contains an internal block 244. The output of the second block is 242, which enters a third block 248. This block contains two parallel vertical blocks, 252 and 250, connected by a horizontal arrow. The output of the third block is 26, which passes through a block 30. A dashed line 238 is also shown near the third block. Other labels include 230, 36, 231, 156, and 240.